

FIG. 1
PRIOR ART

FIG. 2

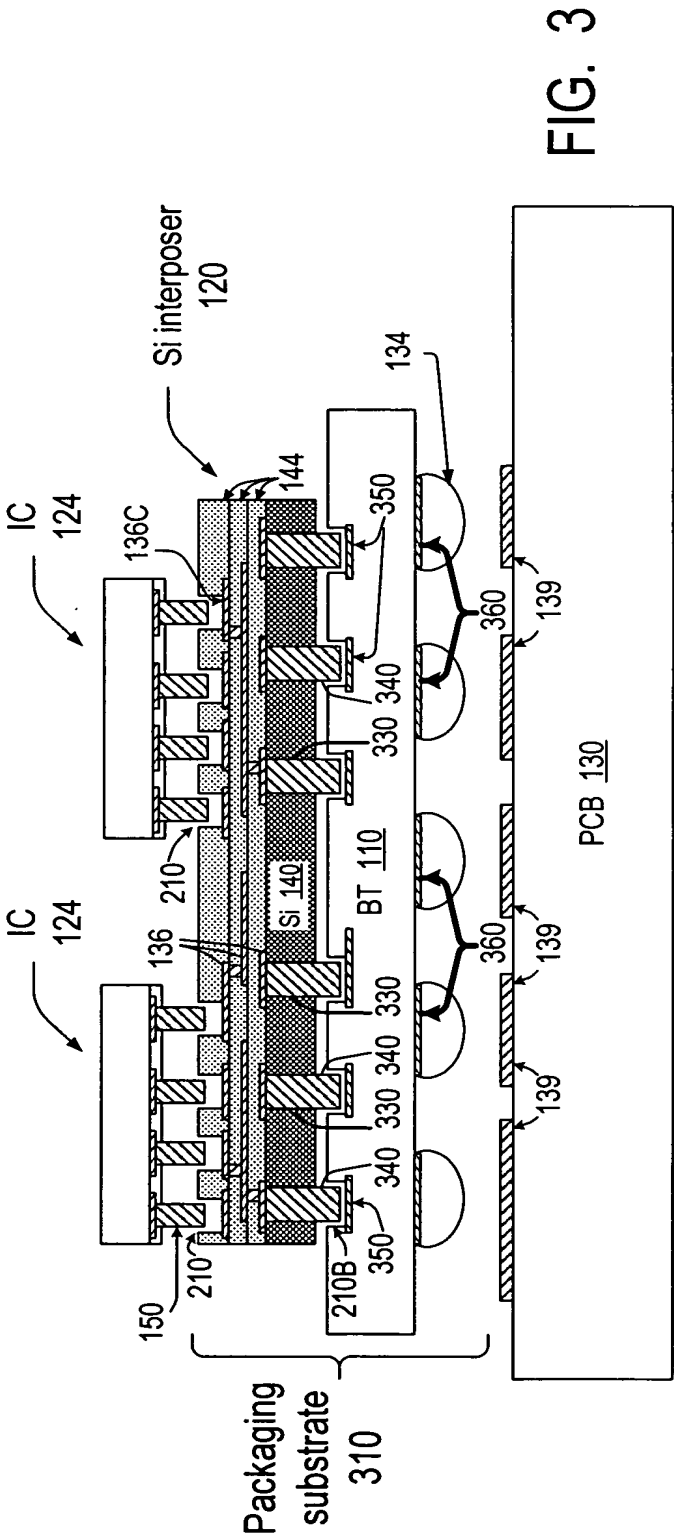
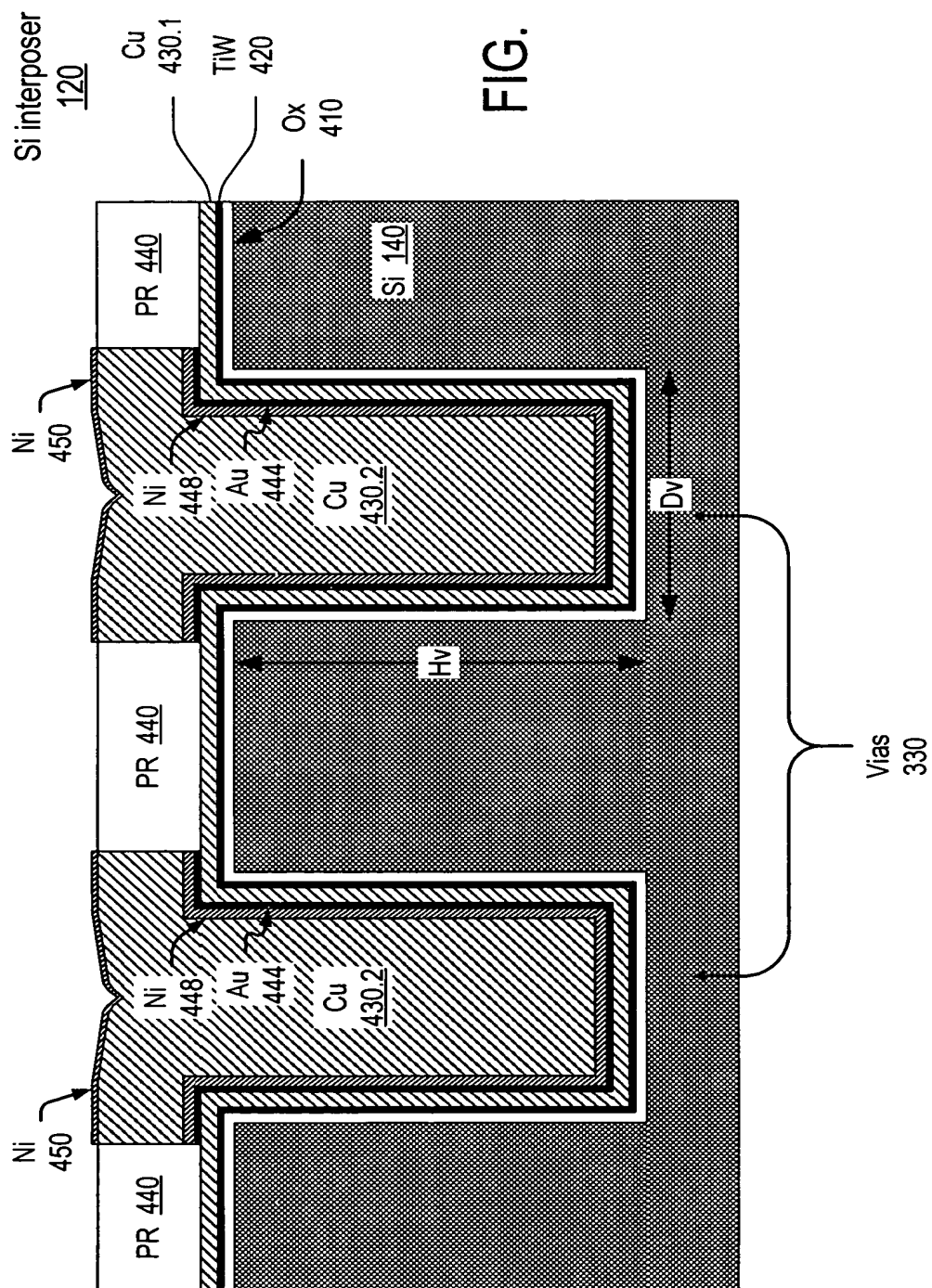
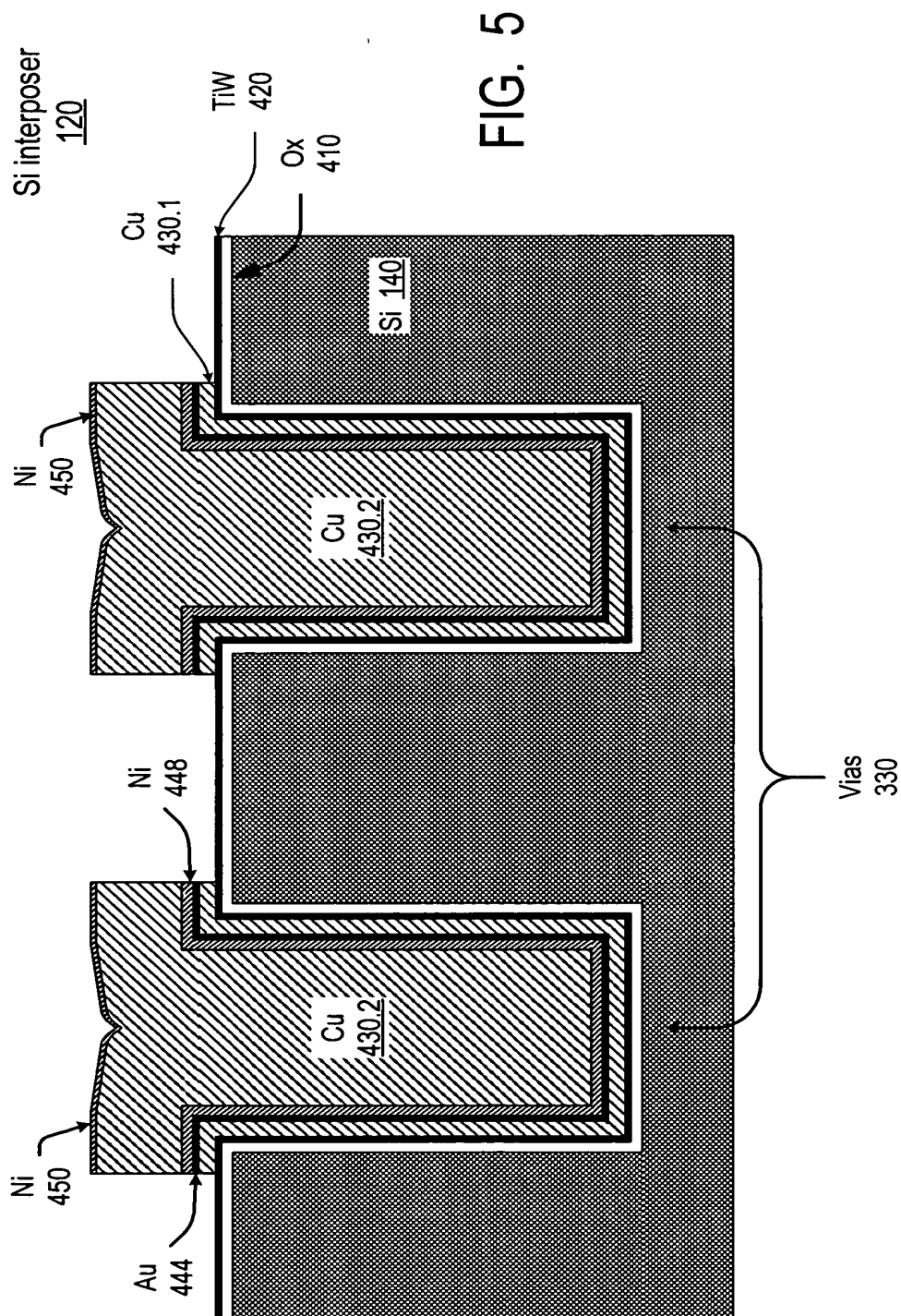


FIG. 3

FIG. 4



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Si interposer
120

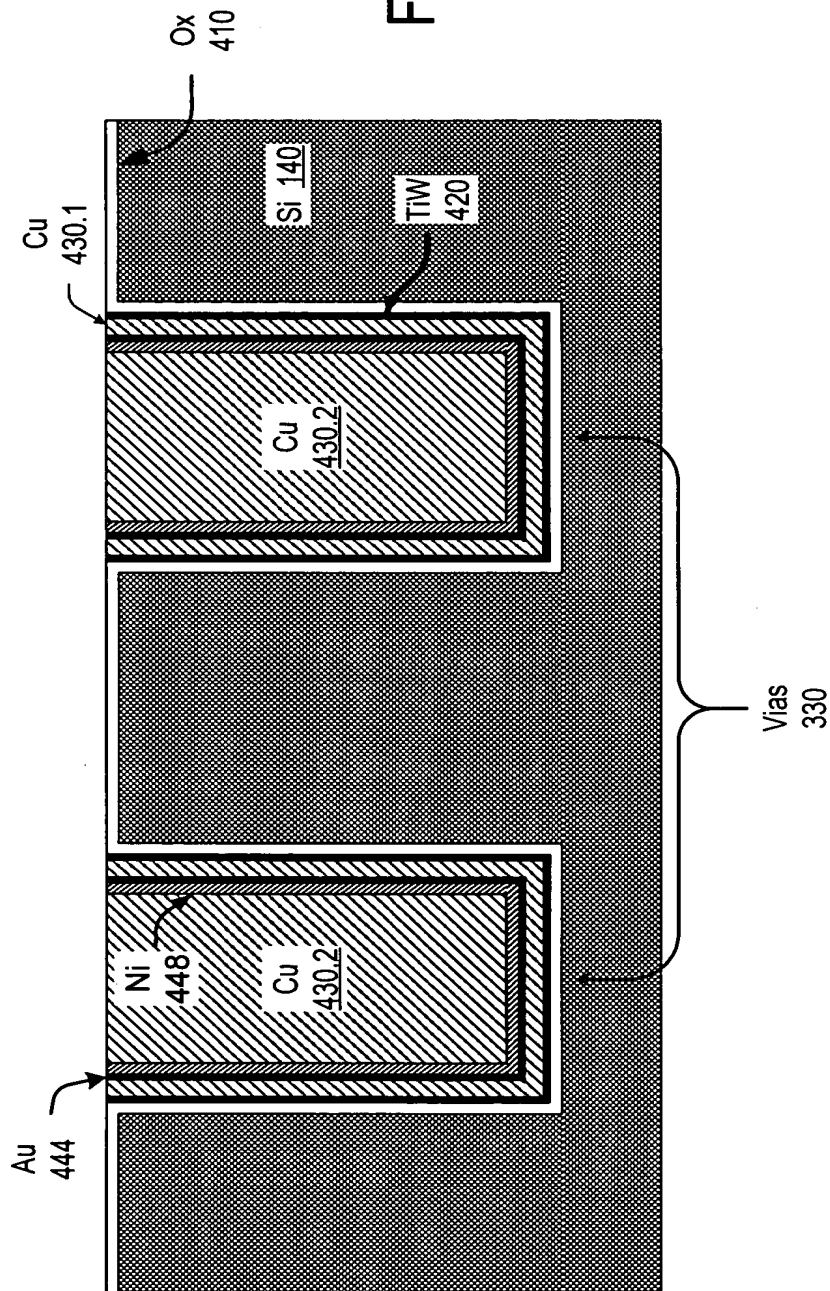


FIG. 6

FIG. 7

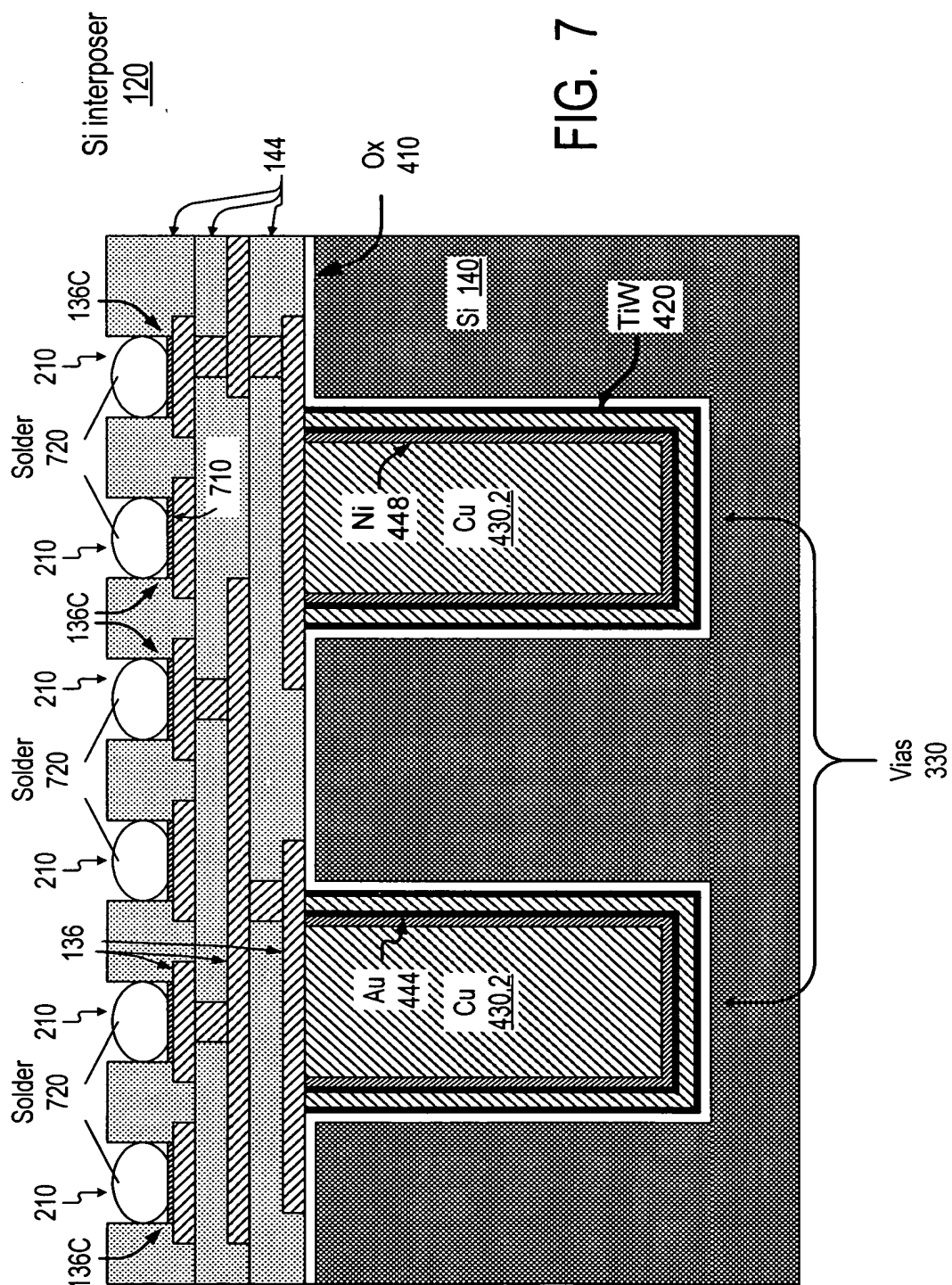
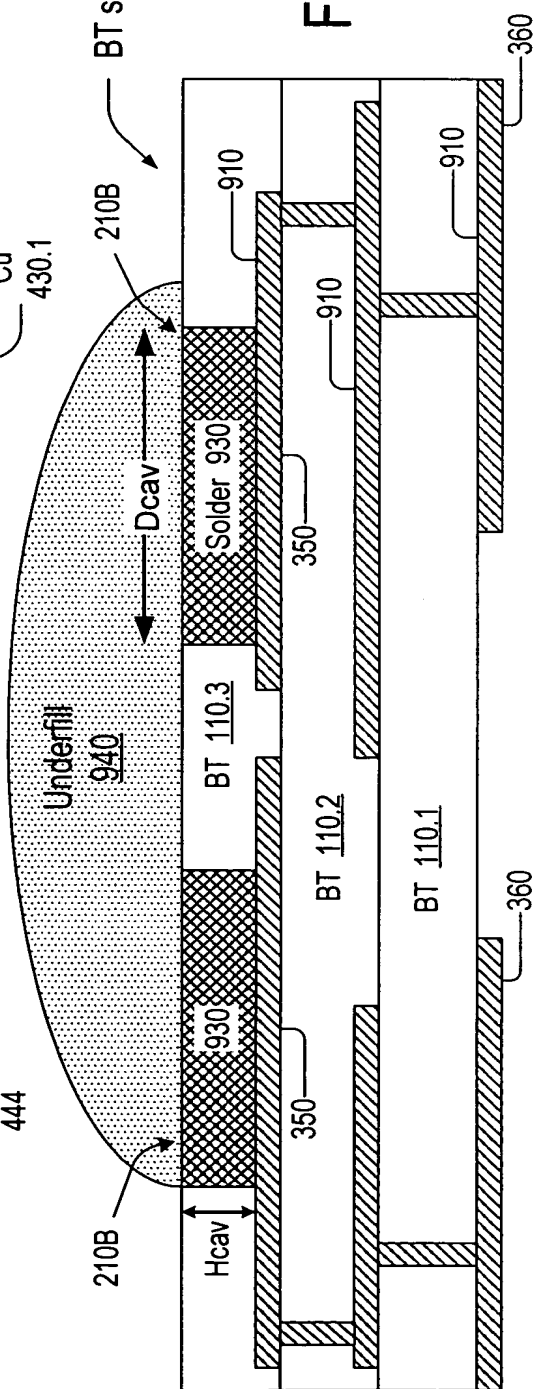


FIG. 8



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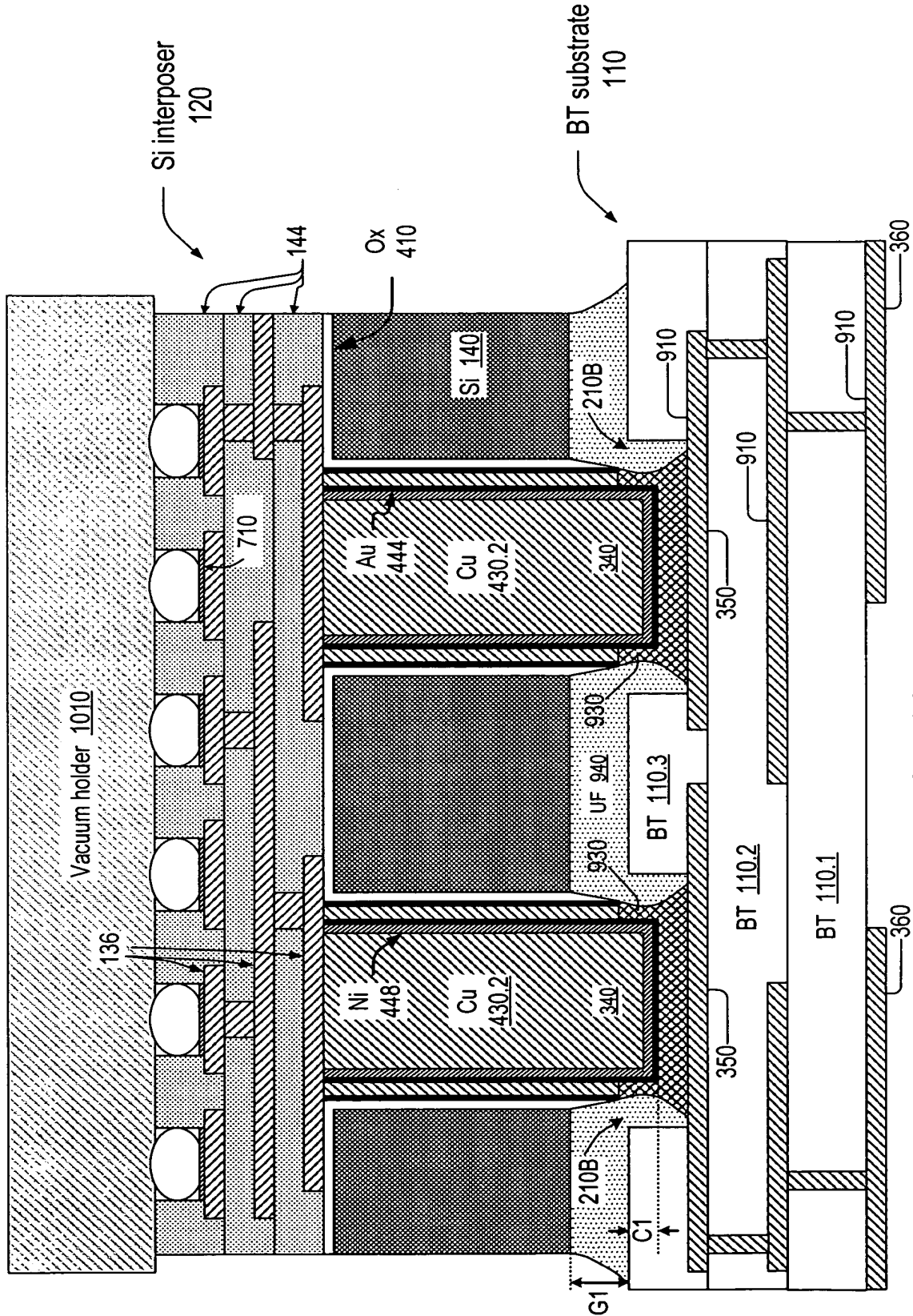


FIG. 10

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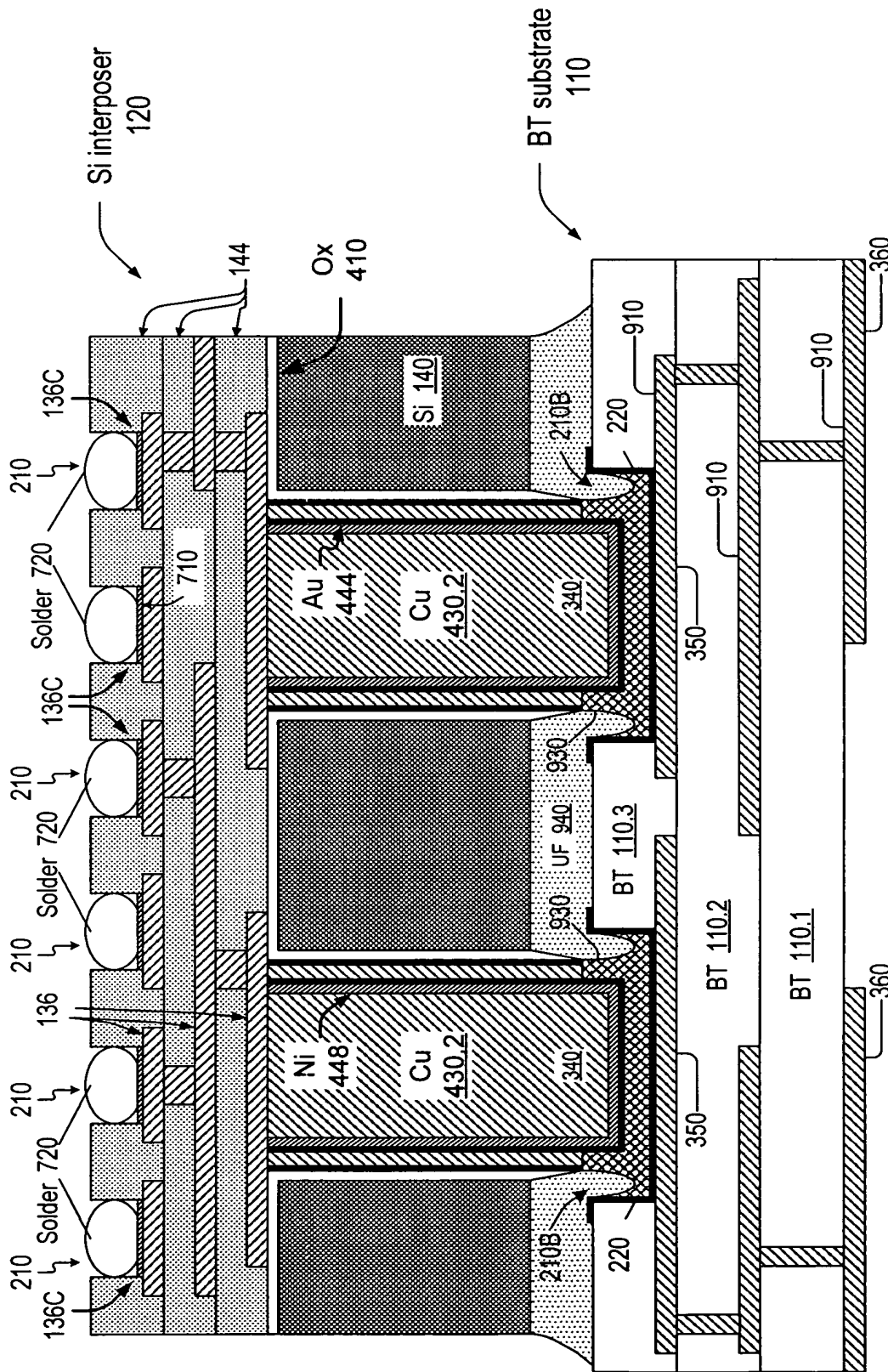


FIG. 11

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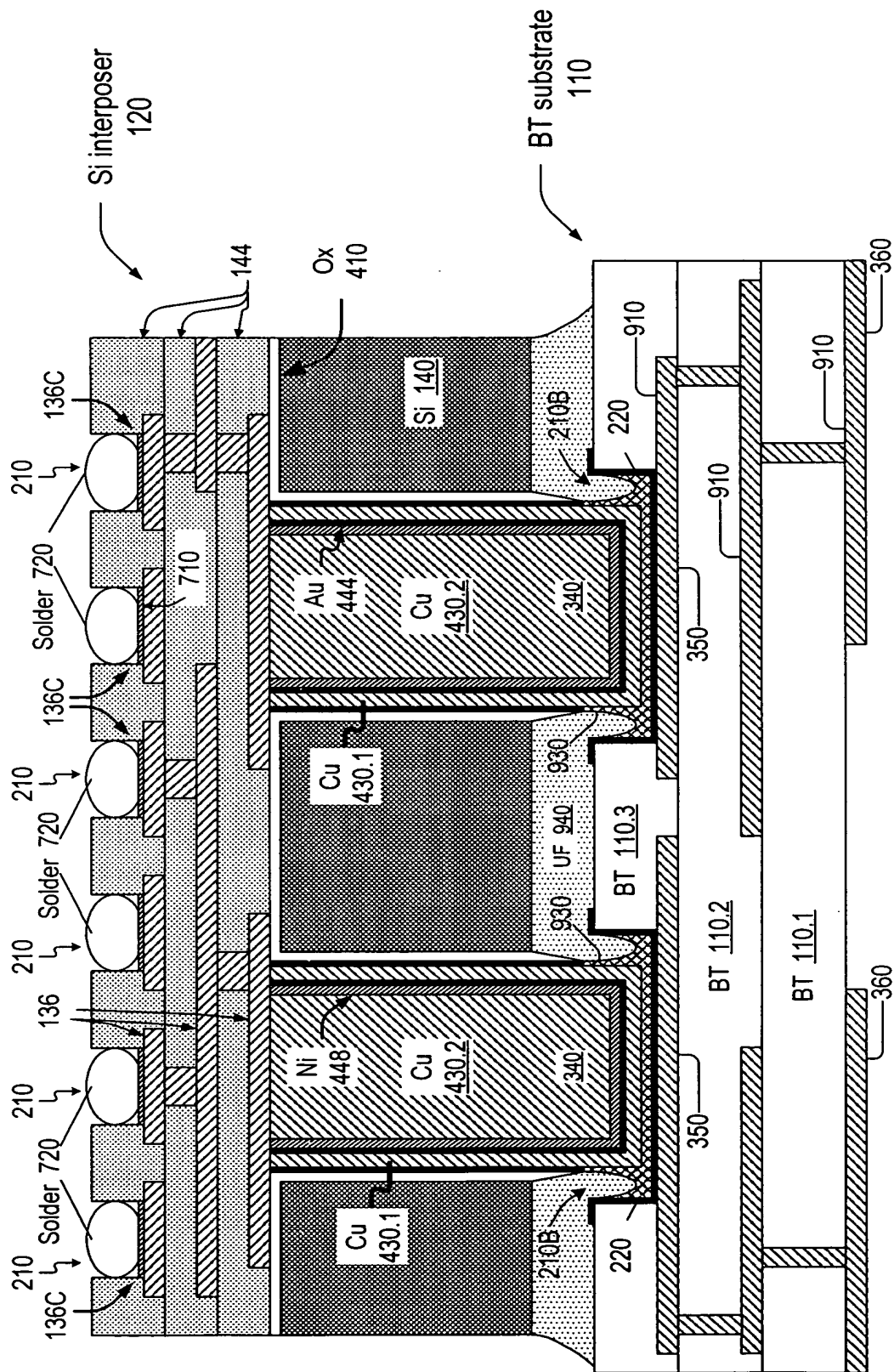
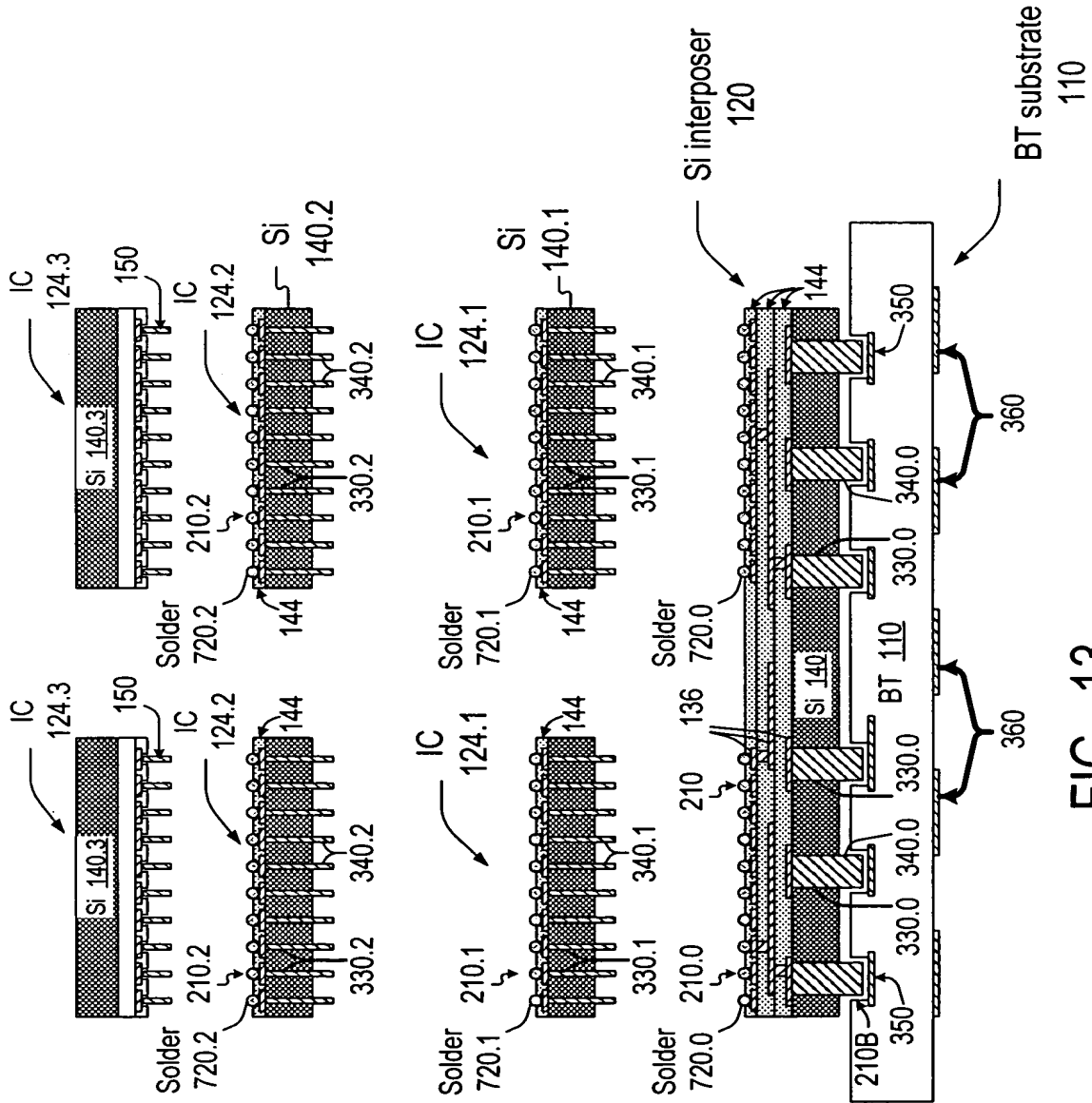
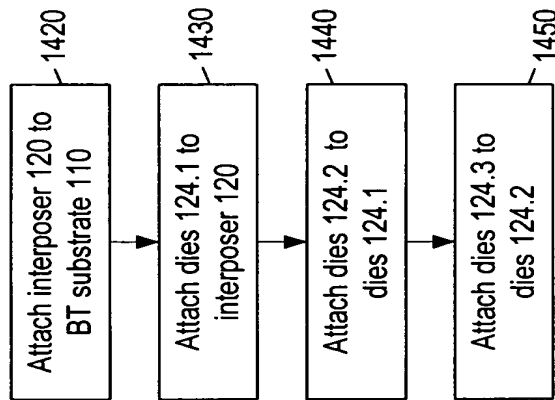
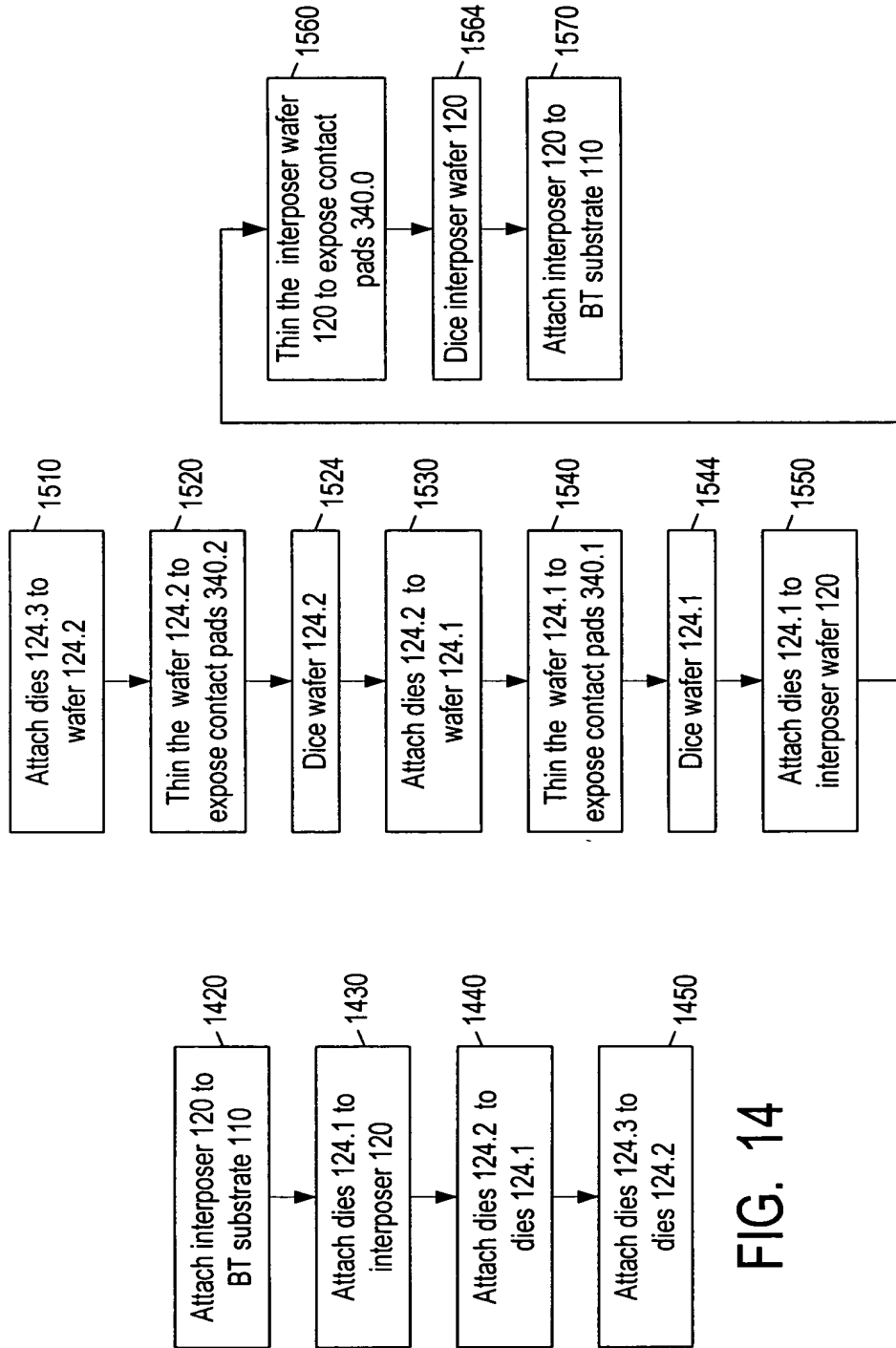


FIG. 12

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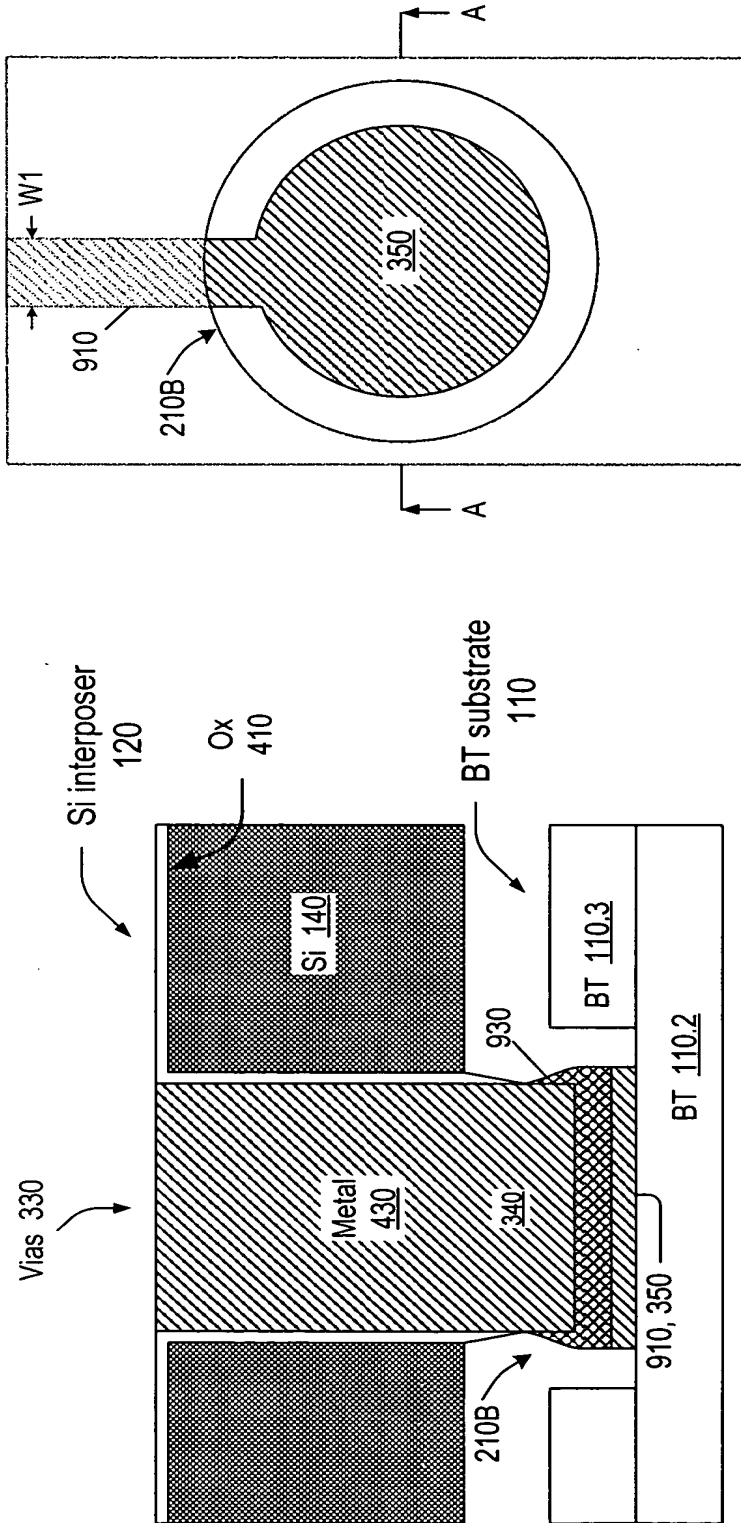
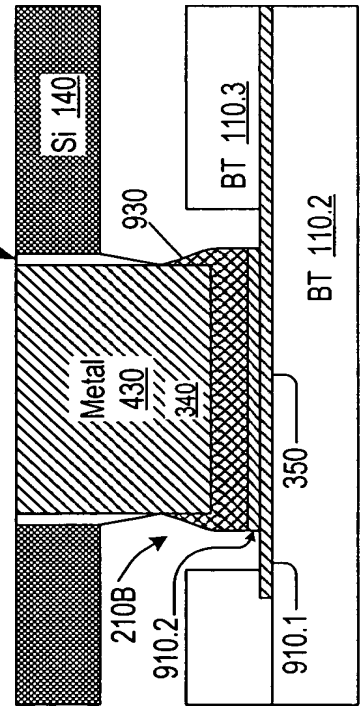


FIG. 16B

Si interposer
120
BT substrate
110

FIG. 17

FIG. 16A



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